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APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
10/054,179	01/17/2002	William R. Wheeler	10559-607001/P12891	4487
20985 7	11/01/2004		EXAMINER	
FISH & RICHARDSON, PC 12390 EL CAMINO REAL			THOMPSON, ANNETTE M	
SAN DIEGO, CA 92130-2081			ART UNIT	PAPER NUMBER
,			2825	
			DATE MAILED: 11/01/2004	

Please find below and/or attached an Office communication concerning this application or proceeding.

·	Application No.	Applicant(s)				
	10/054,179	WHEELER ET AL.				
Office Action Summary	Examiner	Art Unit	m)			
	A. M. Thompson	2825	pr			
The MAILING DATE of this communication app Period for Reply	pears on the cover sheet wit	th the correspondence ac	idress			
A SHORTENED STATUTORY PERIOD FOR REPL THE MAILING DATE OF THIS COMMUNICATION. - Extensions of time may be available under the provisions of 37 CFR 1.1 after SIX (6) MONTHS from the mailing date of this communication. - If the period for reply specified above is less than thirty (30) days, a rep - If NO period for reply is specified above, the maximum statutory period - Failure to reply within the set or extended period for reply will, by statute Any reply received by the Office later than three months after the mailine earned patent term adjustment. See 37 CFR 1.704(b).	136(a). In no event, however, may a re iy within the statutory minimum of thirt will apply and will expire SIX (6) MON a. cause the application to become AB	pply be timely filed y (30) days will be considered time THS from the mailing date of this of the control of t	nly. communication.			
Status						
1) Responsive to communication(s) filed on 17 J	lanuary 2002.					
/ <u>-</u>	s action is non-final.					
closed in accordance with the practice under	Ex parte Quayle, 1935 C.D	. 11, 453 O.G. 213.				
Disposition of Claims						
4)⊠ Claim(s) <u>1-30</u> is/are pending in the application	1.					
4a) Of the above claim(s) is/are withdra	awn from consideration.					
5) Claim(s) is/are allowed.						
6)⊠ Claim(s) <u>1-30</u> is/are rejected.						
7) Claim(s) is/are objected to.						
8) Claim(s) are subject to restriction and/	or election requirement.					
Application Papers						
9) The specification is objected to by the Examin	ner.					
10)⊠ The drawing(s) filed on <u>17 January 2002</u> is/are: a)□ accepted or b)⊠ objected to by the Examiner.						
Applicant may not request that any objection to the			į			
Replacement drawing sheet(s) including the corre						
11) The oath or declaration is objected to by the E	Examiner. Note the attache	d Office Action or form i	PTO-152.			
Priority under 35 U.S.C. § 119						
12) ☐ Acknowledgment is made of a claim for foreig a) ☐ All b) ☐ Some * c) ☐ None of:		§ 119(a)-(d) or (f).	-			
1. Certified copies of the priority document						
2. Certified copies of the priority docume			ni Stago			
3. Copies of the certified copies of the pri		received in this Nation	ai Stage			
application from the International Bureau (PCT Rule 17.2(a)).						
* See the attached detailed Office action for a list of the certified copies not received.						
Attachment(s)						
1) Notice of References Cited (PTO-892)		Summary (PTO-413) (s)/Mail Date				
Notice of Draftsperson's Patent Drawing Review (PTO-948) Information Disclosure Statement(s) (PTO-1449 or PTO/SB/0)	8) 5) Notice of	Informal Patent Application (P	TO-152)			
Paper No(s)/Mail Date 06/11/2003: 03/15/2004 .	6) Other:	 ·				

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DETAILED ACTION

This application 10/054,179 has been examined. Claims 1-30 are pending.

Drawings

The drawings are objected to because Figures 1-4 have poor/illegible figure and 1. line clarity. Corrected drawing sheets in compliance with 37 CFR 1.121(d) are required in reply to the Office action to avoid abandonment of the application. Any amended replacement drawing sheet should include all of the figures appearing on the immediate prior version of the sheet, even if only one figure is being amended. The figure or figure number of an amended drawing should not be labeled as "amended." If a drawing figure is to be canceled, the appropriate figure must be removed from the replacement sheet, and where necessary, the remaining figures must be renumbered and appropriate changes made to the brief description of the several views of the drawings for consistency. Additional replacement sheets may be necessary to show the renumbering of the remaining figures. The replacement sheet(s) should be labeled "Replacement Sheet" in the page header (as per 37 CFR 1.84(c)) so as not to obstruct any portion of the drawing figures. If the changes are not accepted by the examiner, the applicant will be notified and informed of any required corrective action in the next Office action. The objection to the drawings will not be held in abeyance.

Claim Objections

2. Claims 9 and 30 are objected to because of the following informalities: Pursuant to claim 9, the claim preamble should state the intended use or purpose of the

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invention. Pursuant to claim 30, this claim should depend from claim 29. Appropriate correction is required.

Claim Rejections - 35 USC § 102

3. The following is a quotation of the appropriate paragraphs of 35 U.S.C. 102 that form the basis for the rejections under this section made in this Office action:

A person shall be entitled to a patent unless -

(b) the invention was patented or described in a printed publication in this or a foreign country or in public use or on sale in this country, more than one year prior to the date of application for patent in the United States.

Rejection of claims 1-30

- 4. Claims 1-30 are rejected under 35 U.S.C. 102(b) as being anticipated by Watkins et al., U.S. Patent 5,220,512. Watkins discloses a system for simultaneous, interactive presentation of electronic circuit diagrams and simulation data.
- 5. Pursuant to claims 1, 9, 11, 19, 21, 29, Watkins discloses a method (c1, c9), article comprising a machine-readable medium (c11, c19), and apparatus (c21, c29) for modeling a logic design (Abstract, Figs. 1, 2; col. 8, II. 10-48), comprising creating a graphical representation of the logic design (col. 5, II. 26-44); generating simulation code based on the graphical representation (col. 5, II. 26-44; col. 5, line 59 to col. 6, line 13).
- 6. Pursuant to claims 2, 12, and 22, wherein the graphical representation is comprised of functional block diagrams and virtual wires that interconnect the functional block diagrams (Fig. 3; col. 5, II. 26-43).

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- 7. Pursuant to claims 3, 13, 23 wherein creating comprises retrieving the functional block diagrams from a database and arranging the functional block diagrams and the virtual wires to model the logic design (col. 5, II. 45-58).
- 8. Pursuant to claims 4, 14, 24, wherein creating comprises defining the functional block diagrams and the virtual wires to model the logic design (col. 8, II. 10-35).
- 9. Pursuant to claims 5, 15, 25, further comprising displaying a menu comprised of different types of functional block diagrams (col. 5, II. 21-24); receiving an input selecting one of the different types of functional block diagrams; retrieving a selected functional block diagram; and creating the graphical representation of the logic design using the selected functional block diagram (col. 5, II. 26-40).
- 10. Pursuant to claims 6, 16, 26 further comprising propagating a state through the simulation code (col. 5, II. 1-13); and determining if there is an error in the logic design based on the propagated state (col. 6, II. 2-35).
- 11. Pursuant to claims 7, 17, and 27, wherein the state comprises one of a zero, one and an undefined state (Fig. 4, state table).
- 12. Pursuant to claim 8, 18, and 28, further comprising providing a visual indication if there is an error in the graphical representation of the logic design (col. 6, II. 2-6; col. 9, II. 45-57).
- 13. Pursuant to claims 10, 20, and 30, further comprising generating simulation code to simulate the operation of the logic design; and testing the logic design by propagating one or more states through the simulation code (col. 6, II. 6-35; col. 7, II. 3-47).

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Conclusion

14. The prior art made of record and not relied upon is considered pertinent to applicant's disclosure. Please reference the PTO-892 for a complete listing.

15. Any inquiry concerning this communication or earlier communications should be directed to Examiner A.M. Thompson whose telephone number is (571) 272-1909. The Examiner can usually be reached Monday thru Friday from 8:00 a.m. to 4:30 p.m..

Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see http://pair-direct.uspto.gov. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free).

16. Responses to this action should be mailed to the appropriate mail stop:

Mail Stop _____

Commissioner for Patents
P.O. Box 1450

Alexandria, VA 22313-1450
or faxed to:

(703) 872-9306, (for all OFFICIAL communications intended) for entry)

A. M. THOMPSON

Primary Examiner

Technology Center 2800